# Professor Di Wu

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www.unarylab.com

https://scholar.google.com/citations?user=v6DNkTAAAAAJ

4328 Scorpius Street, Orlando, FL, 32816-2362, US

## APPOINTMENT

Assistant Professor | Electrical and Computer Engineering
University of Central Florida

Joint Assistant Professor | Computer Science
University of Central Florida

08/2023 - Present
08/2023 - Present

## RESEARCH INTEREST

- Computer architecture

   performance, efficiency, etc.

   Domain specific acceleration

   artificial intelligence, brain computer interface, etc.
- Emerging computing

   unary, neuromorphic, quantum, photonic, etc.

   Heterogeneous system

   Compute Express Link, multi-GPU, etc.

## **EDUCATION**

Doctor of Philosophy   Electrical and Computer Engineering	09/2017 - 07/2023
University of Wisconsin–Madison  • Advisor: Prof. Joshua San Miguel	
<ul> <li>Thesis: Power-Efficient Computer Architecture via Unary and Approximate Computing</li> </ul>	
<b>8</b> Harold Peterson Outstanding Dissertation Award	
Master of Engineering   Microelectronics	09/2012 – 01/2015
Fudan University	
Bachelor of Science   Microelectronics	09/2007 - 07/2012
Fudan University	
E. D. C.	

# **EMPLOYMENT**

Research Assistant	09/2017 - 07/2023
Department of Electrical and Computer Engineering, UW–Madison  Research Intern	05/2022 - 09/2022, 05/2020 - 09/2020
Cerebras Systems	03/2022 - 03/2022, 03/2020 - 03/2020
Research Intern	05/2019 - 09/2019
Meta (Formerly Facebook)	
Digital Circuit Engineer	03/2015 – 05/2017
HiSilicon	00/0010 01/0010
Research Assistant	09/2012 – 01/2015
State Key Laboratory of ASIC and System, Fudan University	

## HONORS AND AWARDS

Amar Mukherjee Best Paper Award of ISVLSI ☑	2025
IEEE Micro Top Pick Honorable Mention <a>C</a>	2025
Harold Peterson Outstanding Dissertation Award at UW–Madison ✓	2024
Distinguished Artifact Evaluation Award of ASPLOS [2]	2024
Machine Learning and Systems Rising Star ☑	2023
Capstone PhD Teaching Award Nomination at UW-Madison	2022
Grainger Wisconsin Distinguished Graduate Fellowship at UW-Madison	2022
Student Travel Award of ISCA	2022
Dissertator Travel Award at UW-Madison	2022
Ph.D. Forum of DAC	2021
IEEE Micro Top Pick ☑	2021
Gerald Holdridge Outstanding Teaching Assistant Award at UW-Madison	2020
Chancellor's Opportunity Fellowship at UW-Madison	2019
Qualcomm Innovation Fellowship Finalist 🗹	2019
Foxconn SmartCity Competition Winner	2019
Student Research Travel Award at UW-Madison	2019
Student Research Competition Travel Award of ASPLOS	2019

Last update: July, 2025

Student Travel Award of ASPLOS	2019
Hiran Mayukh Award at UW–Madison 🗹	2018
Rising Star Award at HiSilicon	2015
National Scholarship at Fudan University (1/67)	2015
Excellent Student Union Leader at Fudan University	2010
Third Prize Freshman Scholarship at Fudan University (3/45)	2007

#### **PUBLICATIONS**

+ - Student \* - Collaborator × - Advisor

#### Conference

- [1] Can Photonic Interconnects be used for High-Throughput Memory Access in FHE Accelerators? Dewan Saiham, Mariam Rabadi, **Di Wu**, Sazadur Rahman\*

  International Symposium on Low Power Electronics and Design, 2025
- [2] Catwalk: Unary Top-K for Efficient Ramp-No-Leak Neuron Design for Temporal Neural Networks Devon Lister<sup>+</sup>, Prabhu Vellaisamy, John Shen\*, **Di Wu**IEEE Computer Society Annual Symposium on VLSI, 2025
  - **&** Amar Mukherjee Best Paper Award
- [3] Leveraging Photonic Interconnects for Scalable and Efficient Fully Homomorphic Encryption Dewan Saiham, **Di Wu**, Sazadur Rahman\*

  Government Microcircuit Applications & Critical Technology Conference, 2025
- [4] LoAS: Fully Temporal-Parallel Datatflow for Dual-Sparse Spiking Neural Networks Ruokai Yin, Youngeun Kim, Di Wu, Priyadarshini Panda\*
  - The International Symposium on Microarchitecture, 2024, DOI: 10.1109/MICRO61859.2024.00084
- [5] Exploration of Unary Arithmetic-Based Matrix Multiply Units for Low Precision DL Accelerators Prabhu Vellaisamy, Harideep Nair, **Di Wu**, Shawn Blanton\*, John Paul Shen\* *IEEE Computer Society Annual Symposium on VLSI*, 2024, DOI: 10.1109/ISVLSI61997.2024.00126
- [6] ALISA: Accelerating Large Language Model Inference via Sparsity-Aware KV Caching Youpeng Zhao, Di Wu, Jun Wang\*
  - ♦ International Symposium on Computer Architecture, 2024, DOI: 10.1109/ISCA59077.2024.00077
- [7] Carat: Unlocking Value-Level Parallelism for Multiplier-Free GEMMs Zhewen Pan, Joshua San Miguel<sup>×</sup>, **Di Wu** 
  - ♦ International Conference on Architectural Support for Programming Languages and Operating Systems, 2024, DOI: 10.1145/3620665.3640364
  - **TEEE Micro Top Pick Honorable Mention (24 from all computer architecture papers)**
  - **&** Distinguished Artifact Evaluation Award

Open-source software: artifact

- [8] uBrain: A Unary Brain Computer Interface
  - $\mathbf{Di}$  Wu, Jingjie Li, Zhewen Pan, Younghyun Kim\*, Joshua San Miguel $^{\times}$
  - Thernational Symposium on Computer Architecture, 2022, DOI: 10.1145/3470496.3527401
- [9] uSystolic: Byte-Crawling Unary Systolic Array Di Wu, Joshua San Miguel<sup>×</sup>
  - ♦ International Symposium on High-Performance Computer Architecture, 2022, DOI: 10.1109/HPCA53966.2022.00010
    Open-source software: uSystolic-Sim 🗹
- [10] When Dataflows Converge: Reconfigurable and Approximate Computing for Emerging Neural Networks  $\mathbf{Di}$  Wu, Joshua San Miguel $^{\times}$ 
  - International Conference on Computer Design, 2021, DOI: 10.1109/ICCD53106.2021.00014
- [11] UNO: Virtualizing and Unifying Nonlinear Operations for Emerging Neural Networks **Di Wu**, Jingjie Li, Setareh Behrooz, Younghyun Kim\*, Joshua San Miguel<sup>×</sup> *International Symposium on Low Power Electronics and Design*, 2021, DOI: 10.1109/ISLPED52811.2021.9502473
- [12] Normalized Stability: A Cross-Level Design Metric for Early Termination in Stochastic Computing  $\mathbf{Di}$  Wu, Ruokai Yin, Joshua San Miguel $^{\times}$ 
  - Asia and South Pacific Design Automation Conference, 2021, DOI: 10.1145/3394885.3431549
- [13] uGEMM: Unary Computing Architecture for GEMM Applications Di Wu, Jingjie Li, Ruokai Yin, Hsuan Hsiao, Younghyun Kim\*, Joshua San Miguel<sup>×</sup>
  - ♦ International Symposium on Computer Architecture, 2020, DOI: 10.1109/ISCA45697.2020.00040
  - **&** IEEE Micro Top Pick (12 from all computer architecture papers)

Open-source software: UnarySim 🗹

- [14] Approximate Hardware Techniques for Energy-Quality Scaling Across the System Younghyun Kim\*, Joshua San Miguel\*, Setareh Behroozi, Tianen Chen, Kyuin Lee, Yongwoo Lee, Jingjie Li, **Di Wu** *International Conference on Electronics, Information, and Communication*, 2020, DOI: 10.1109/ICEIC49074.2020.9051208
- [15] SECO: A Scalable Accuracy Approximate Exponential Function Via Cross-Layer Optimization Di Wu, Tianen Chen, Chienfu Chen, Oghenefego Ahia, Joshua San Miguel<sup>×</sup>, Mikko Lipasti\*, Younghyun Kim\* International Symposium on Low Power Electronics and Design, 2019, DOI: 10.1109/ISLPED.2019.8824959
- [16] In-Stream Stochastic Division and Square Root via Correlation Di Wu, Joshua San Miguel<sup>×</sup> Design Automation Conference, 2019, DOI: 10.1145/3316781.3317844
- [17] Convergence-Optimized Variable Node Structure for Stochastic LDPC Decoder Qichen Zhang, Yun Chen, Di Wu, Xiaoyang Zeng, Yeong-luh Ueng International Conference on Acoustics, Speech and Signal Processing, 2016, DOI: 10.1109/ICASSP.2016.7472936
- [18] An Area-Efficient Architecture for Stochastic LDPC Decoder Qichen Zhang, Yun Chen, **Di Wu**, Xiaoyang Zeng, Yeong-luh Ueng *International Conference on Digital Signal Processing*, 2015, DOI: 10.1109/ICDSP.2015.7251868
- [19] Latency-Optimized Stochastic LDPC Decoder for High-Throughput Applications **Di Wu**, Yun Chen, Qichen Zhang, Lirong Zheng, Xiaoyang Zeng, Yeong-luh Ueng *International Symposium on Circuits and Systems*, 2015, DOI: 10.1109/ISCAS.2015.7169329
- [20] A High-Throughput LDPC Decoder for Optical Communication Di Wu, Yun Chen, Yuebin Huang, Yeongluh Ueng, Lirong Zheng, Xiaoyang Zeng International Conference on ASIC, 2013, DOI: 10.1109/ASICON.2013.6811973

# **Journal**

- [1] Synergizing Quantum Techniques with Machine Learning for Advancing Drug Discovery Challenge Zhiding Liang, Zichang He, Yue Sun, Dylan Herman, Qingyue Jiao, Yanzhang Zhu<sup>+</sup>, Weiwen Jiang\*, Xiaowei Xu\*, **Di Wu**, Marco Pistoia\*, Yiyu Shi\*

  Scientific Reports 14 (2025), p. 31216, DOI: 10.1038/s41598-024-82576-4
- [2] uGEMM: Unary Computing for GEMM Applications
   Di Wu, Jingjie Li, Ruokai Yin, Hsuan Hsiao, Younghyun Kim\*, Joshua San Miguel\*
   IEEE Micro 41.3 (2021), pp. 50–56, DOI: 10.1109/MM.2021.3065369
   IEEE Micro Top Pick (12 from all computer architecture papers)
- [3] In-Stream Correlation-Based Division and Bit-Inserting Square Root in Stochastic Computing Di Wu, Ruokai Yin, Joshua San Miguel<sup>×</sup> *IEEE Design & Test* 38.6 (2021), pp. 53–59, DOI: 10.1109/MDAT.2021.3050716
- [4] Strategies for Reducing Decoding Cycles in Stochastic LDPC Decoders
  Di Wu, Yun Chen, Qichen Zhang, Yeong-luh Ueng, Xiaoyang Zeng

  IEEE Transactions on Circuits and Systems II: Express Briefs 63.9 (2016), pp. 873–877, DOI: 10.1109/TCSII.2016.2535038
- [5] An Efficient Multirate LDPC-CC Decoder With a Layered Decoding Algorithm for the IEEE 1901 Standard Yun Chen, Qichen Zhang, **Di Wu**, Changsheng Zhou, Xiaoyang Zeng

  IEEE Transactions on Circuits and Systems II: Express Briefs 61.12 (2014), pp. 992–996, DOI: 10.1109/TCSII.2014.2362721

## Workshop

- [1] Syndrilla: Simulating Decoders for Quantum Error Correction using PyTorch Yanzhang Zhu<sup>+</sup>, Chen-Yu Peng, Yun Hao Chen, Siyuan Niu\*, Yeong-Luh Ueng\*, **Di Wu** *IEEE International Conference on Quantum Computing and Engineering*, 2025 Open-source software: Syndrilla ☑
- [2] Synergizing Error Suppression, Mitigation and Correction for Fault-Tolerant Quantum Computing Yanzhang Zhu<sup>+</sup>, Siyuan Niu<sup>\*</sup>, **Di Wu**IEEE Workshop on Quantum Intelligence, Learning & Security, collocated with International Conference on Trust, Privacy and Security in Intelligent Systems, and Applications (2024), DOI: 10.1109/TPS-ISA62245.2024.00065
- [3] Exploration of Unary Arithmetic-Based Matrix Multiply Units for Low Precision DL Accelerators Prabhu Vellaisamy, Harideep Nair, **Di Wu**, Shawn Blanton\*, John Paul Shen\*

  Workshop on Unary Computing, collocated with International Conference on Architectural Support for Programming Languages and Operating Systems (2024)
- [4] xBrain: Brain-Like Computing for Explainable Brain-Computer Interfaces
  Queenly Xie<sup>+</sup>, Prabhu Vellaisamy, **Di Wu**Young Architect Workshop, collocated with International Conference on Architectural Support for Programming Languages and Operating Systems (2024)

# [5] T-MAC: Temporal Multiplication with Accumulation

Zhewen Pan, **Di Wu**, Joshua San Miguel<sup>×</sup>

Young Architect Workshop, collocated with International Conference on Architectural Support for Programming Languages and Operating Systems (2022)

# **Pre-Print**

- [1] Unleashing The Potential of LLMs for Quantum Computing: A Study in Quantum Architecture Design Zhiding Liang, Jinglei Cheng, Rui Yang, Hang Ren, Zhixin Song, **Di Wu**, Tongyang Li\*, Yiyu Shi\* arXiv Pre-print (2023)
- [2] Representation Range Needs for 16-Bit Neural Network Training Valentina Popescu\*, Abhinav Venigalla\*, **Di Wu**, Robert Schreiber\* arXiv Pre-print (2021)

Open-source software: Industry adoption: Automatic Mixed Precision – cbfloat16 🗹

# **INVITED TALKS**

Salvage Deep Learning Efficiency: A Unary Computing Approach University of California, Santa Cruz Shanghai Tech University Fudan University Case Western Reserve University Peking University University of Minnesota Twin Cities University of Louisiana at Lafayette Unary Computing for Power-Efficient Computer Architecture AMD Research University of Central Florida University of California, Los Angeles University of California, Santa Barbara  TEACHING AND MENTORING	02/2025 12/2024 12/2024 10/2024 08/2024 03/2024 11/2023 07/2023 02/2023 11/2022 10/2022
TEACHING AND MENTORING	
Instructor  EEL4742C (Embedded Systems), UCF EEE3342C (Digital Systems), UCF EEL5796 (Big Data Computer Architecture and Systems), UCF ECE697 (Capstone Project in Machine Learning and Signal Processing), UW–Madison	SP 2025 FA 2024 SP 2024 SU 2023
Teaching Assistant  ECE554 (Digital Engineering Lab), UW-Madison ECE454 (Mobile Computing Lab), UW-Madison ECE454 (Mobile Computing Lab), UW-Madison ECE554 (Digital Engineering Lab), UW-Madison ECE552 (Introduction to Computer Architecture), UW-Madison	SP 2022 FA 2021 FA 2020 SP 2020 FA 2019 SP 2019 FA 2018 FA 2018
Guest Lecturer ECE18743 (Neuromorphic Computer Architecture & Processor Design), CMU ECE757 (Advanced Computer Architecture II), UW–Madison ECE757 (Advanced Computer Architecture II), UW–Madison ECE752 (Advanced Computer Architecture I), UW–Madison	SP 2025 SP 2021 SP 2020 SP 2019
PROFESSIONAL SERVICE	
Panelist  NSF Medium Panel in Division of Computer and Network Systems (CNS) IEEE Workshop on Quantum Intelligence, Learning & Security (QUILLS)  Committee  Organizing Chair of Workshop on Architecting Error Corrected Quantum Computers (ARQTEC) at Organizing Chair of Undergrad Panel on "Charging STEM Career" at UCF Organizing Chair of Workshop on Unary Computing (WUC) at ASPLOS Organizing Committee of Quantum Computing for Drug Discovery Challenge at ICCAD Program Committee of ASPLOS Program Committee of HPCA	HPCA 2025 2024 2024 2024 2024 2023 2026 2024, 2025, 2026
Program Committee of IFCA Program Committee of ISCA Program Committee of MICRO Program Committee of ISWC Program Committee of ISPASS Program Committee of IPDPS	2024, 2025, 2026 2024, 2025 2025 2023, 2025 2024, 2025 2025

Program Committee of DAC	2025
Program Committee of ICCAD	2024
Program Committee of DAC PhD Forum	2024
Program Committee of ICCD	2023, 2024
Program Committee of ICRC	2024
Program Committee of ICA3PP	2023
Program Committee of IEEE Workshop on Quantum Intelligence, Learning & Security	
Program Committee of Young Architect Workshop (YArch)	2023, 2024, 2025
Program Committee of ASPLOS Artifact Evaluation	2020, 2021
Program Committee of MICRO Artifact Evaluation	2021
Mentor	. 2024
UCF IEEE Engineering in Medicine and Biology Society (EMBS) Student Branch Chap	
Computer Architecture Long-term Mentoring (CALM)	2024
Young Architect Workshop (YArch)	2023
Undergrad Architecture Mentoring Workshop (uArch) MICRO – "Meet a Senior PhD Student"	2023, 2024 2020
	2020
Journal Reviewer	
ACM Transactions on Architecture and Code Optimization (TACO)	
ACM Transactions on Embedded Computing Systems (TECS)	
ACM Transactions on Reconfigurable Technology and Systems (TRETS) IEEE Computer Architecture Letter (CAL)	
IEEE Computer Architecture Letter (CAL)  IEEE Transactions on Circuits and Systems I (TCAS-I)	
IEEE Transactions on Computers (TC)	
IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TC	AD)
IEEE Transactions on Emerging Topics in Computing (TETC)	and)
IEEE Transactions on Very Large Scale Integration (VLSI) Systems (TVLSI)	
IEEE Signal Processing Letters (SPL)	
Journal of Network and Computer Applications (JNCA)	
npj Quantum Information	
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Funding	
Awarded	
AMD Developer Cloud Credit: \$2k	2025
AMD AI & HPC Fund (GPU node hours)	2024, 2025
AMD Fund for Academic Research (Unrestricted Gift): \$100k	2024
Quantum Computing Access at NERSC (QCAN) Program	2025
STUDENTS	
51UDEN15	
Current Students	
Daniel Price (PhD)	2024 – Present
Marco Kurzynski (PhD)	2024 – Present
Yanzhang Zhu (PhD)	2024 – Present
Zubaidah Al-Mashhadani (PhD)	2024 – Present
Former Students	
Parker McLeod (UG, AMD, full-time)	2023 - 2024
Tyler Goldsmith (UG, AMD, full-time)	2023 - 2024
Mustafa Nisar (UG, AMD, full-time)	2023 - 2024
Zhewen Pan (MS, UW–Madison, PhD)	2020 - 2022
Ruokai Yin (UG, Yale, PhD)	2019 – 2021
Reference	
	DID 1:
Professor Joshua San Miguel	PhD advisor
jsanmiguel@wisc.edu	University of Wisconsin–Madison
Professor Younghyun Kim younghyun.kim@wisc.edu	PhD committee member University of Wisconsin–Madison
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jpshen@cmu.edu	Carnegie Mellon University
Professor Timothy Sherwood	G
	versity of California, Santa Barbara
Professor Ulya R. Karpuzcu	•
	niversity of Minnesota, Twin Cities